

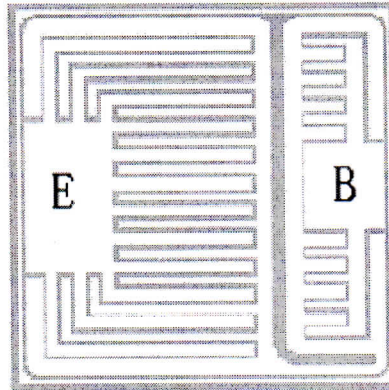


# Sierra Components, Inc.

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Chip back potential is the level which bulk silicon is maintained by on-chip connection, or it is the level to which the chip back must be connected when specifically stated below. If no potential is given the chip back should be isolated.



**Top Material: Al**  
**Backside Material: Ag**  
**Bond Pad Size: .004" X .004"**  
**Backside Potential: Collector**  
**Mask Ref:**

**APPROVED BY: DK**

**DIE SIZE .136" X .136"**

**DATE: 5/24/13**

**MFG:ST Micro**

**THICKNESS .015"**

**P/N: 2N6059**